

### III. Amendments to the Specification

Please insert on Page 1, before the paragraph starting on line 1 "Background."

Please insert on Page 1, before the paragraph starting on line 9 "Summary"

Please insert on Page 8, before the paragraph starting on line 19 "Brief Description of the Drawings."

Please insert on Page 8 before the paragraph starting of line 5 "Detailed Description."

Please replace the paragraph on page 8, lines 31-38 with the following amended paragraph:

Figure 1 shows an integrated test circuit arrangement 10 arranged on a substrate 11 for example on a square silicon chip having edge lengths L that are smaller than ten millimeters. A connection region 14 containing a plurality of connections 16 to 26 that are electrically insulated from one another is arranged along an edge 12. The function of the connections 16 to 26 is explained in more detail below with reference to Figure 2.